

### Amendments to the Claims

This listing of claims will replace all prior versions, and listings of claims in the application:

#### **Listing of Claims:**

Claim 1 (Currently Amended): A level shift circuit comprising:

a first transistor circuit which conducts is conductive between a first node and a first power supply line when a second node is at a second power supply potential, and is [[does]] not conductive therebetween conduct them when said second node is at a first power supply potential;

A3  
a second transistor circuit which is conductive between conducts said second node and said first power supply line when said first node is at said second power supply potential, and is [[does]] not conductive therebetween conduct them when said first node is at said first power supply potential;

a third transistor circuit which is conductive between conducts said first node and [[said]] a second power supply line when an input signal is at a first input potential, and is [[does]] not conductive therebetween conduct them when said input signal is at a second input potential;

a fourth transistor circuit which is conductive between conducts said second node and said second power supply line when said input signal is at a second input potential, and is [[does]] not conductive therebetween conduct them when said input

signal is at said first input potential; and

a fifth transistor circuit which switches a value of an inflow current or emission current of said second node or said first node according to a control signal, when said second node or said first node is conductive conducted to both of said first power supply line and said second power supply line,

wherein said first transistor circuit comprises a first conductive type first transistor one end of which is connected to said first power supply line, an other end of which is connected to said first node, and a control terminal of which is connected to said second node,

AS  
said second transistor circuit comprises a first conductive type second transistor one end of which is connected to said first power supply line, an other end of which is connected to said second node, and a control terminal of which is connected to said first node,

said third transistor circuit comprises a second conductive type third transistor one end of which is connected to said second power supply line, an other end of which is connected to said first node, and a control terminal of which has said input signal provided thereto, and

said fourth transistor circuit comprises a second conductive type fourth transistor one end of which is connected to said second power supply line, an other end of which is connected to said second node, and a control terminal of which has an inverted value of said input signal provided thereto,

wherein said fifth transistor circuit comprises

a second conductive type fifth transistor one end of which is connected to  
said second power supply line and a control terminal which has said control  
signal input thereto, and

A3  
a second conductive type sixth transistor one end of which is connected to  
an other end of said fifth transistor, an other end of which is connected to  
said second node, and a control terminal which has the inverted value  
of said input signal provided thereto.

Claims 2-25 (Cancelled)